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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/199,776	11/25/1998	SANG-HAE LEE	P55394	4064

7590 04/23/2003

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EXAMINER
ZAMANI, ALI A

ART UNIT	PAPER NUMBER
2674	16

DATE MAILED: 04/23/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 09/199,776	Applicant(s) LEE, SANG-HAE	
	Examiner Ali A. Zamani	Art Unit 2674	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *04 December 2002* .

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *2, 6-9 and 21-70* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *2, 6-9 and 21-70* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11)☐ The proposed drawing correction filed on _____ is: a)☐ approved b)☐ disapproved by the Examiner.

If approved, corrected drawings are required in reply to this Office action.

12)☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

13)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____ .

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

14)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).

a) ☐ The translation of the foreign language provisional application has been received.

15)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

1) ☐ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____ .
4) ☐ Interview Summary (PTO-413) Paper No(s). _____ .
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: .

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless -
>
> (e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371© of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) do not apply to the examination of this application as the application being examined was not (1) filed on or after November 29, 2000, or (2) voluntarily published under 35 U.S.C. 122(b). Therefore, this application is examined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

2. Claims 26-35 are rejected under 35 U.S.C. 102(e) as being anticipated by Parks et al. (US Pat. No. 5,483,260).

In regard to claims 26-35, Parks et al. teach a method comprising: connecting a video display unit (116) to a computer system (114) after computer system has been powered on and initialized, video display unit (116) is connected to computer system (114); when said video display unit (116) is detected as being connected to computer system (114), reading first data

corresponding to said video display unit; determining whether said first data corresponds to second data stored in the memory unit, storing said first data in memory unit (CMOS memory) and identifying a resolution corresponding to video display unit and transmitting said resolution to a video card coupled to video display unit (116). Furthermore, no, non-volatile memory is required in the video display unit (116), but rather the settings are preferably stored in the CMOS memory inside the computer system and also, at each power-on of the computer system (114), the data path between the keyboard controller (252) and the video display is selected to enable the display unit to provide capability information to the system unit such as resolution, interface method, and refresh frequency. Thus, this information is automatically provided to the system unit (114) without direct involvement (col. 3, lines 23-43).

3. Claims 2, 6-9, 21-25, 36-42 and 43-70 are rejected under 35 U.S.C. 103(a) as being unpatentable over Parks et al. In view of Uehara et al. (US Pat. No. 5,488,384).

In regard to claims 2, 6-9, 21-25, 36-42 and 43-70, Parks et al. teach a method, comprising: connecting a video display unit to a computer system unit which provides bi-directional communication which enables the video monitor to inform the system unit of its capabilities without direct user involvement and also enables the system unit to directly control or adjust all the functions of the video monitor and control software is included in the system unit which can be used to control or adjust the output of the video monitor (see the abstract), the method for simplified video monitor control in a computer system comprising a system unit

including a CPU (222), memory storing monitor control software, and a communication port, the computer system also including a video monitor coupled to display unit and input device coupled to the system unit, wherein the video monitor includes logic for controlling the visual output of the video monitor, receiving monitor control input data from a user, storing the monitor control input data in the memory in the system unit selecting a first data path between the communication port and the video monitor, wherein the communication port includes first data path between the communication port and video monitor and a second data path between the communication port and a peripheral device and the system unit providing the monitor control input data to the video monitor using first data path between the communication port and the video monitor, and the video monitor adjusting its visual output according to said monitor control data (see Figs. 2-5).When a user desires to adjust the output of the video monitor, the monitor control software directs the multiplexers to provide a communication path between the system unit and video monitor (selects either first data path between port and peripheral device or second data path between port and video monitor) and the monitor control software also generates a user interface on the video monitor that enables a user to easily adjust the operation of the monitor and a non-volatile memory also coupled to communication port which receives and stores video capability information and video monitor transmits said video monitor capability information through said communication port to CPU (222) (see Fig. 4) without direct user involvement and during power-on of the computer system. Also, at each power-on of the computer system which correspondence to a polling operation, the data path between the

keyboard controller and video monitor is selected to enable the video monitor to provide capability information to system unit such as resolution, interface method, and refresh frequency. Thus, this information is automatically provided to the system unit without direct user involvement (see col. 5, lines 20-39). Parks et al. do not espscifically mentioned the rebooting process, but Parks mentions about power-on procedure. It would have been obvious at the time of the invention was made to utilize the power-on steps for the purpose of the desired booting process. Parks et al. Substantially teach the above claimed limitations except for teaching "determing whether said first data corresponds to second data stored in memory". However, Uehara et al. teach a computer control system having a flat panel as standard equipment and allowing a CRT as an optional unit to be connected thereto, reading first data corresponding to video display unit (RGB) in all the palettes in a RAMDAC (see the abstract). Furthermore, the display signal levels to a CRT, comprising the steps of a writing first test data in the plurality of color registers, and determing on the basis of R, G, and B display signals levels output from D/A converter whether a color CRT is connected and writing second test data in the plurality of color registers and determining on the basis of RGB display signal levels output from D/A converter whether a monochrome CRT is connected. Moreover, the status register (59) for laching an output from the comparator (63) is arranged in the monitor detector (57) and comparison result from the comarator (63) is directly output to the VGA (51). Thus, it would have been obvious to one of ordinary skill in the art to utilize the system of Uehara et al. In the method of Parks et al. to provide a display system and method thereof, in a computer system which cannot

simultaneously operate a standard display unit and optional display unit in which when the system is set up while the power is ON.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ali Zamani whose telephone number is (703) 308-6414. The examiner can normally be reached on Monday through Friday from 8:00 a.m. to 5:00 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard A. Hjerepe, can be reached on (703) 305-4709.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, DC 20231

or faxed to:

(703) 872-9314 (for Technology Center 2600 only)

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.

Ali Zamani

April 04, 2003

RICHARD HJERPE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600